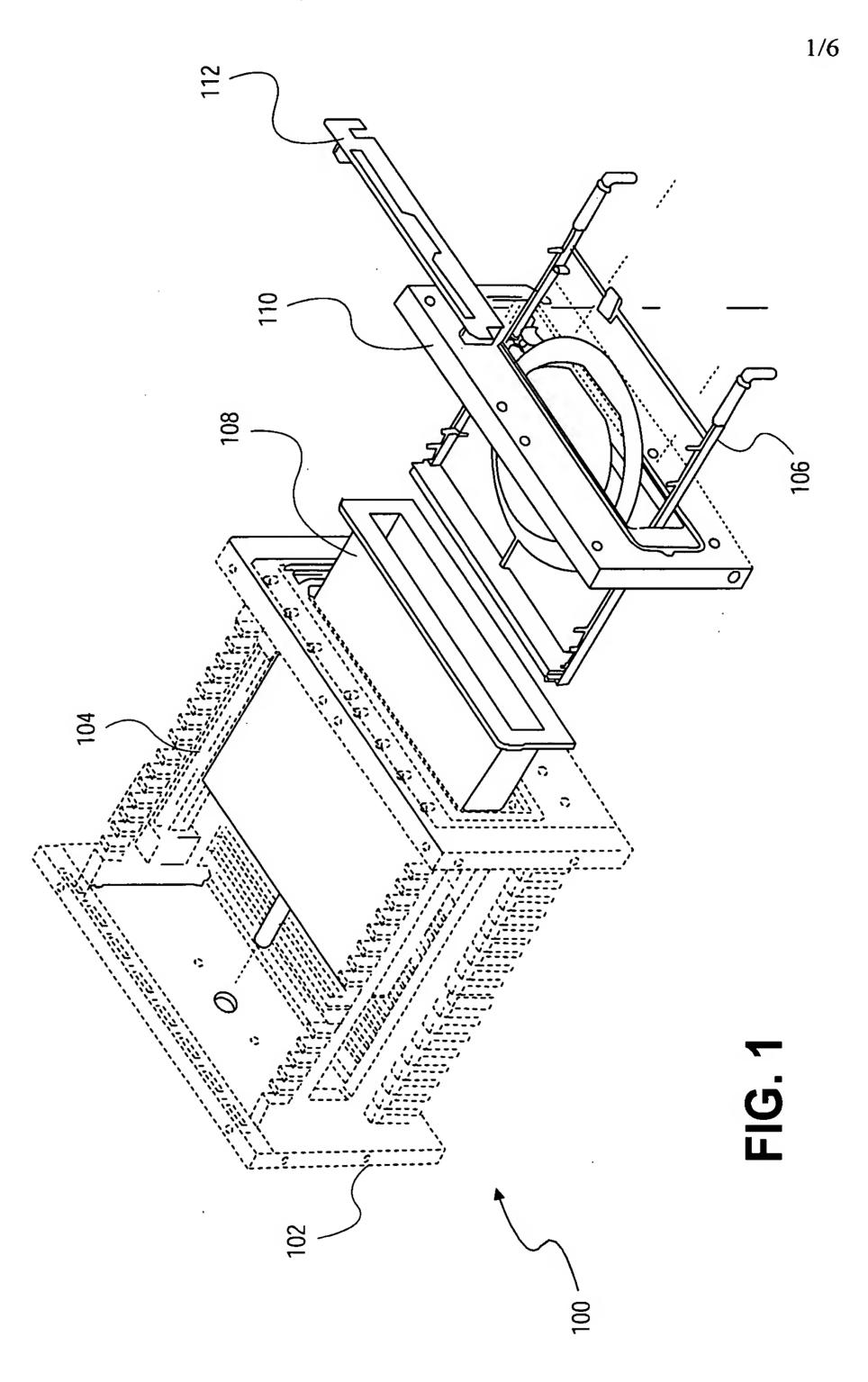
For: Semiconductor Wafer Tray Positioning Attorney Docket No.: 67,200-627





For: Semiconductor Wafer Tray Positioning Attorney Docket No.: 67,200-627



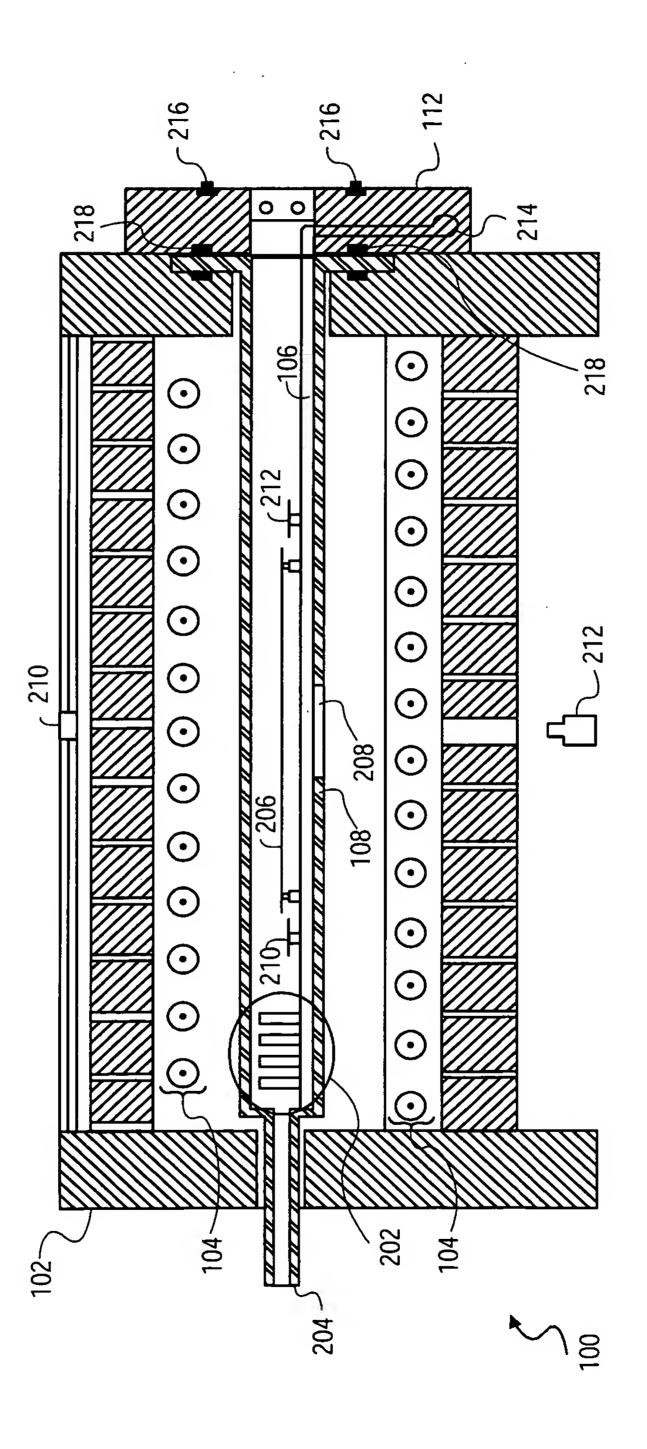
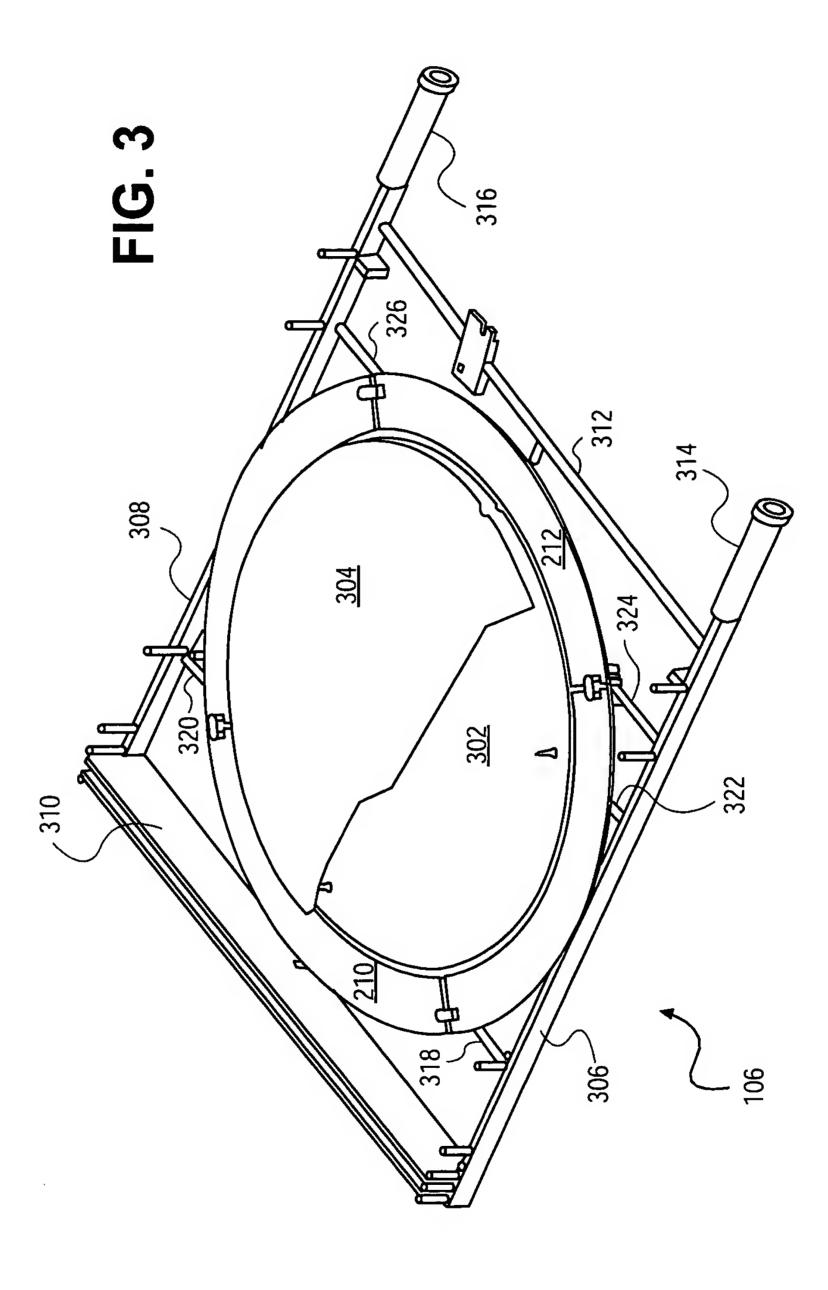


FIG. 2

For: Semiconductor Wafer Tray Positioning Attorney Docket No.: 67,200-627

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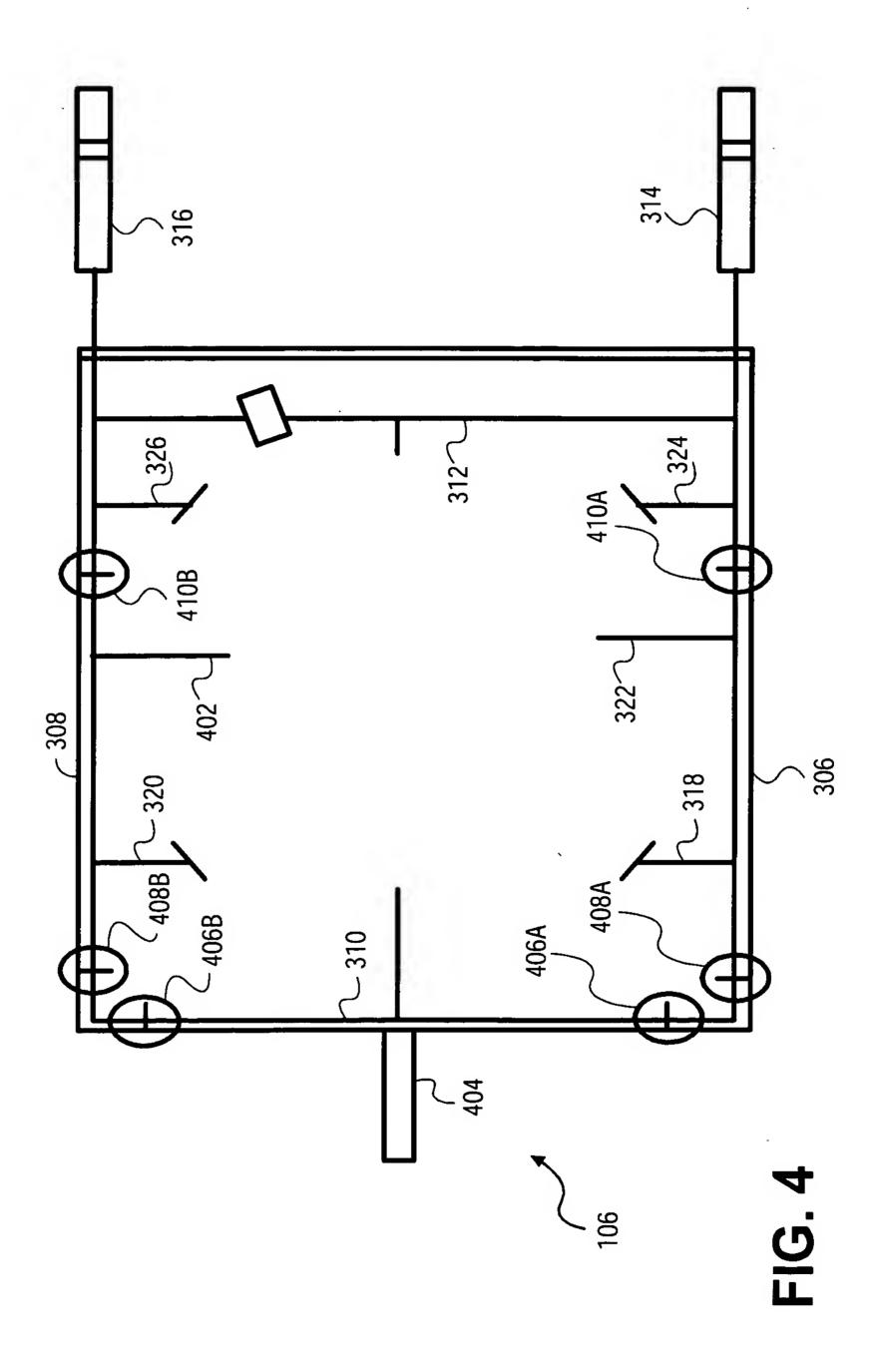
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Inventor(s): Pan
Serial No.: To Be Assigned
Filed: Herewith

For: Semiconductor Wafer Tray Positioning Attorney Docket No.: 67,200-627



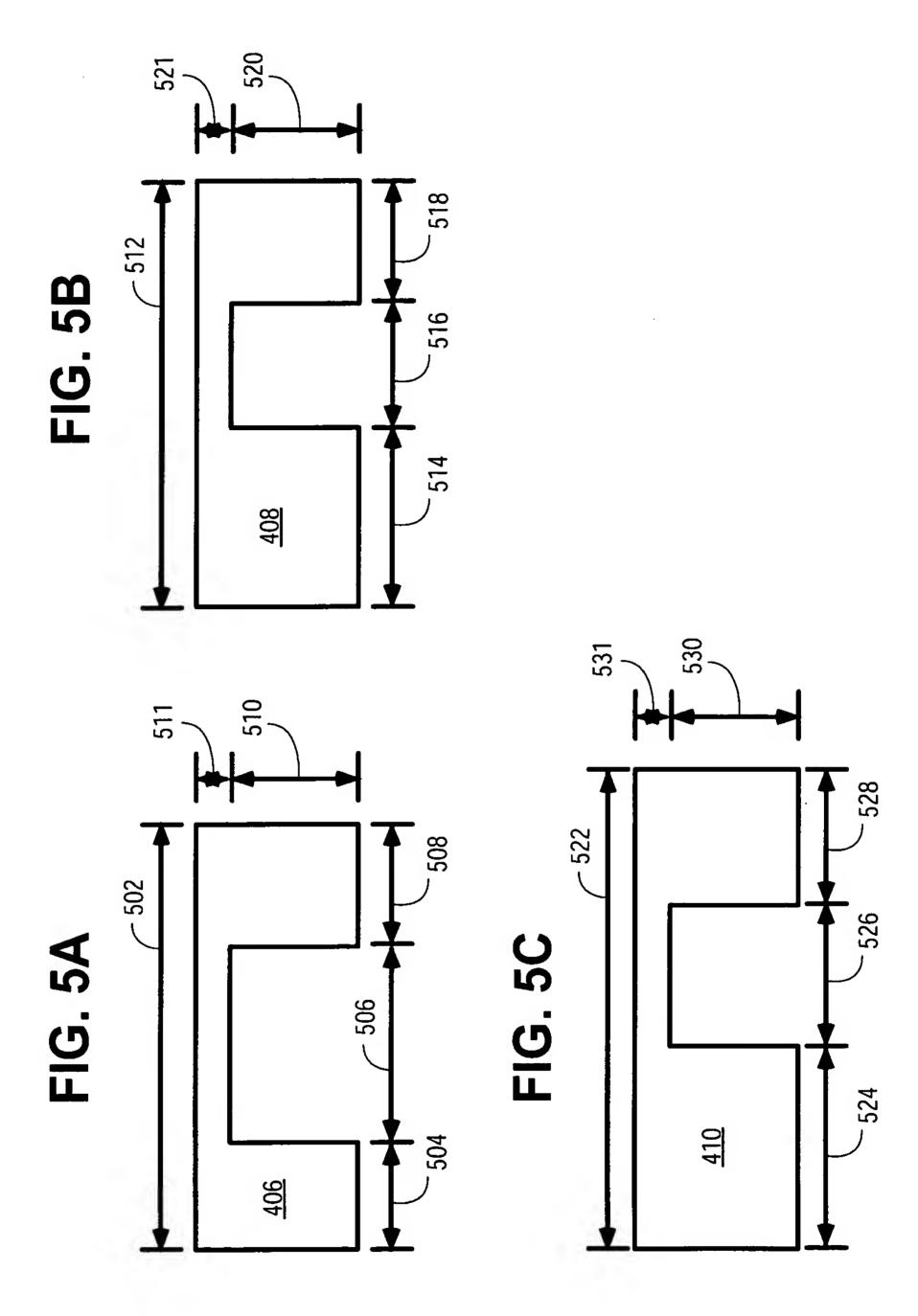
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Inventor(s): Pan
Serial No.: To Be Assigned
Filed: Herewith
For: Semiconductor Wafer Tray Positioning
Attorney Docket No.: 67,200-627

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For: Semiconductor Wafer Tray Positioning Attorney Docket No.: 67,200-627

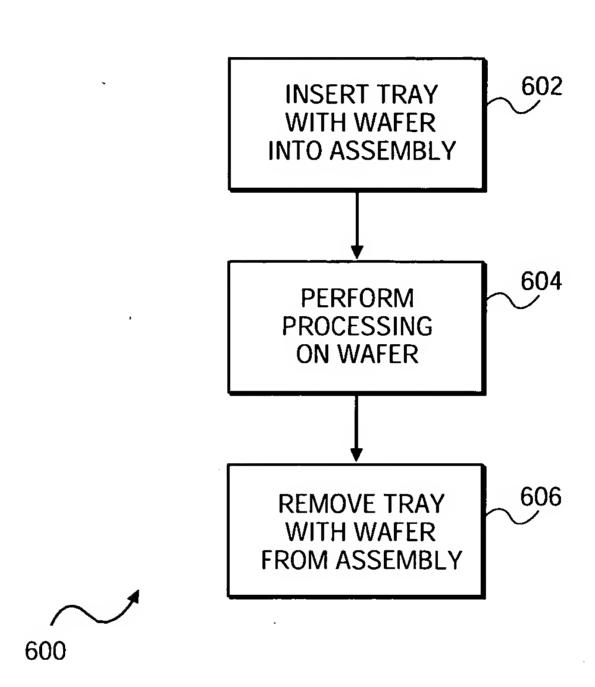


FIG. 6